

**IN THE SPECIFICATION:**

Page 1, please insert the following as the first paragraph:

This application is a U.S. National Phase Application  
under 35 USC 371 of International Application  
PCT/JP2003/013819 filed October 29, 2003.

Please replace the paragraph at page 5, lines 30-31, with  
the following amended paragraph:

~~FIG. 9 is a circuit arrangement drawing showing an~~  
~~example~~ Figs. 9A and 9B are circuit arrangement drawings showing  
examples of the detailed configuration of the logic circuit  
applicable to the specified state setting section of the current  
generation circuit in this embodiment.

Please replace the paragraph at page 6, lines 23-24, with  
the following amended paragraph:

~~FIG. 26 is a circuit arrangement drawing showing one~~  
~~example~~ Figs. 26A and 26B are circuit arrangement drawings  
showing examples of the inverted latch circuit applied to the  
data driver in this embodiment and the selection setting circuit.